

ARA: Static Initialization of Dynamically-Created System Objects

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supported by **DFG**

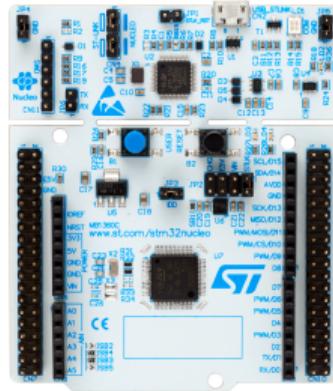
```
QueueHandle_t q1;

void f1() { xQueueSend(q1, create()); }
void f2() { msg_t e; while(xQueueReceive(q1, &e)) process(e); }

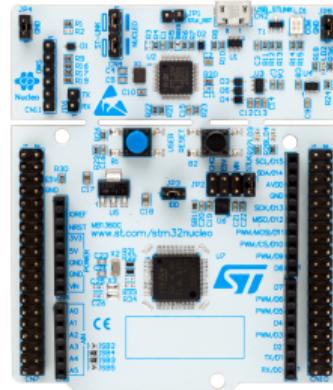
void init_os() {
    xTaskCreate("T1", f1, 4);
    xTaskCreate("T2", f2, dyn_prio());
    q1 = xQueueCreate(10, sizeof(msg_t));
}

int main() {
    init_serial();
    init_os();
    printf("System ready!");
    vTaskStartScheduler();
}
```

- Run the example on a Nucleo STM32-F103RB
- Boot time: **40,048 cycles**



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- Unwanted **dynamic** OS overhead
- Do the same thing every boot again
- First deadline to reach



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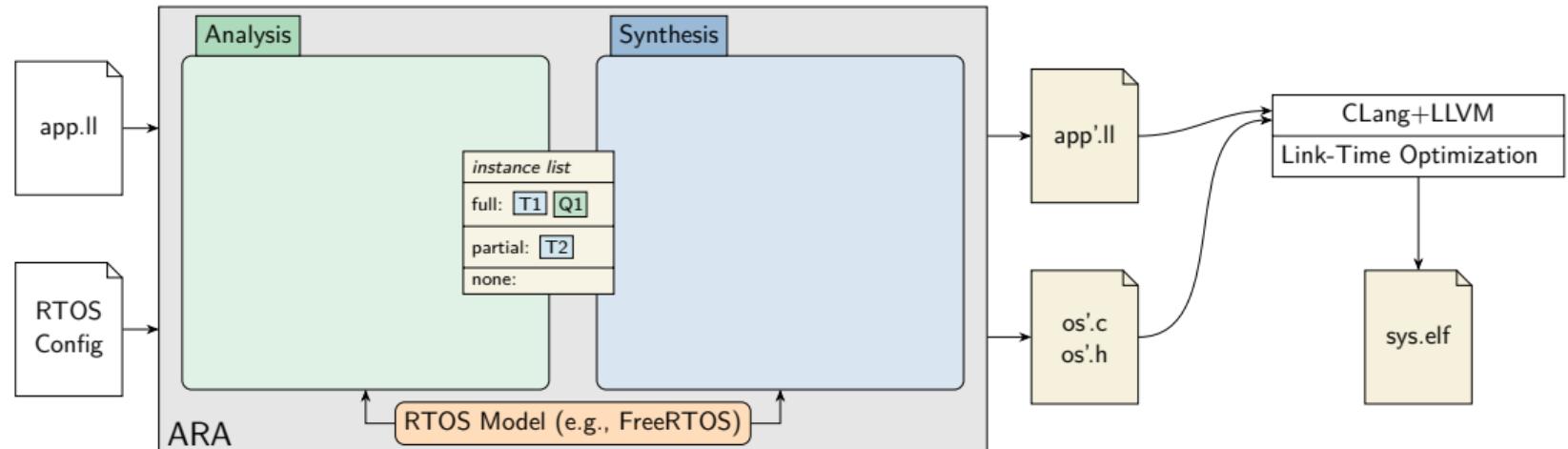
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ARA

- Whole system optimizer
- Static initialization
- Fully automatic and OS independent





```
QueueHandle_t q1;

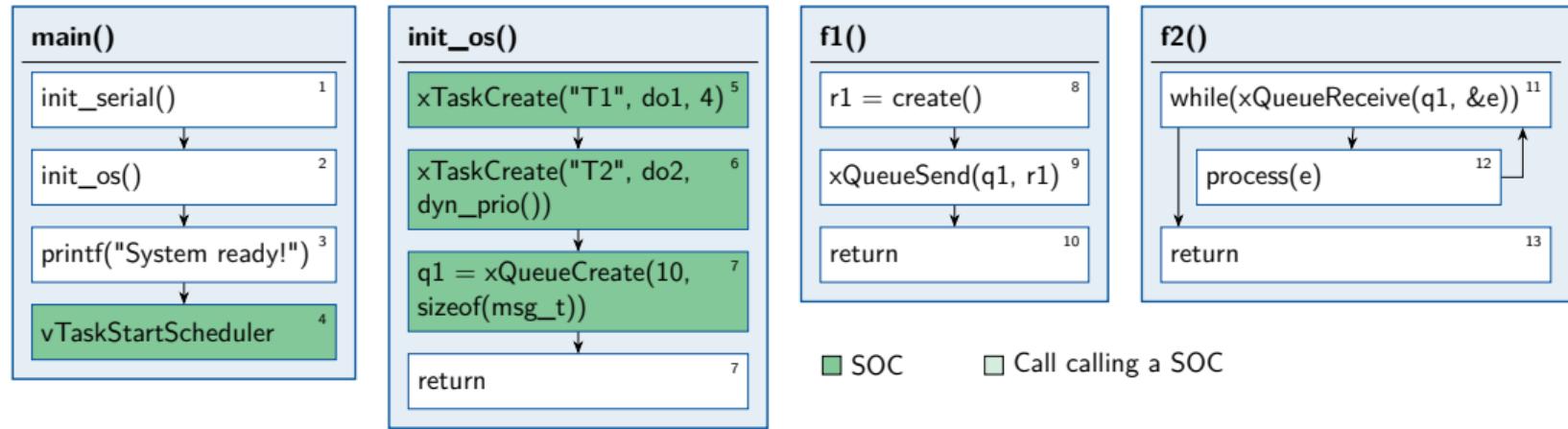
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```

System Objects	T1	T2	Q1
Created before scheduler	✓	✓	✓
Created exactly once	✓	✓	✓
RTOS allocates memory	✓	✓	✓
All parameters known	✓	✗	✓

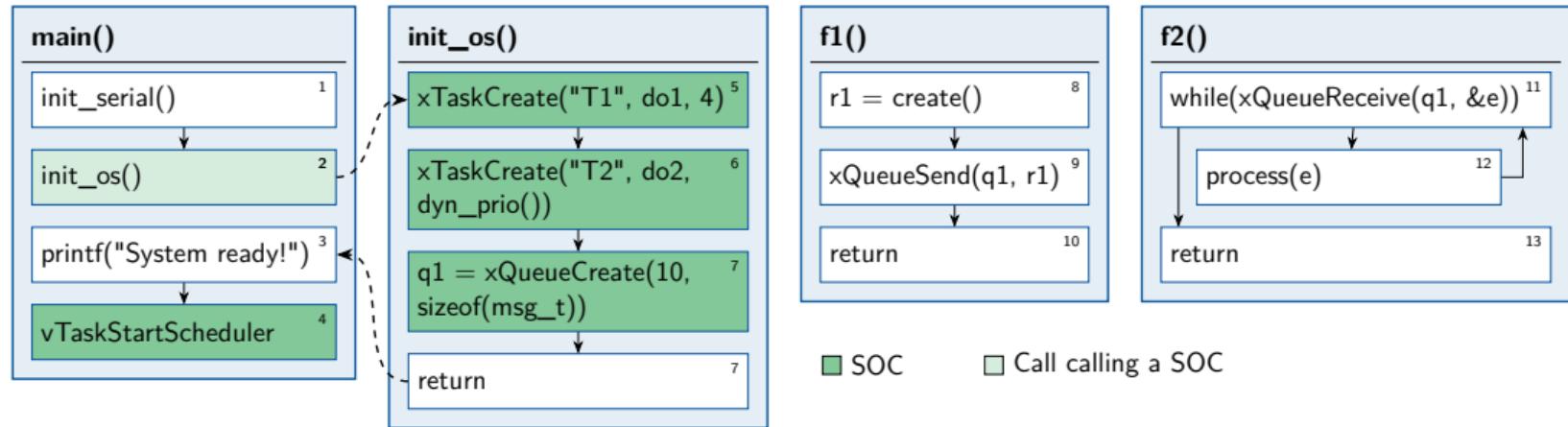
How to get it automatically?



Difficulty: Function Pointer

Simple pointer: Andersen's pointer analysis

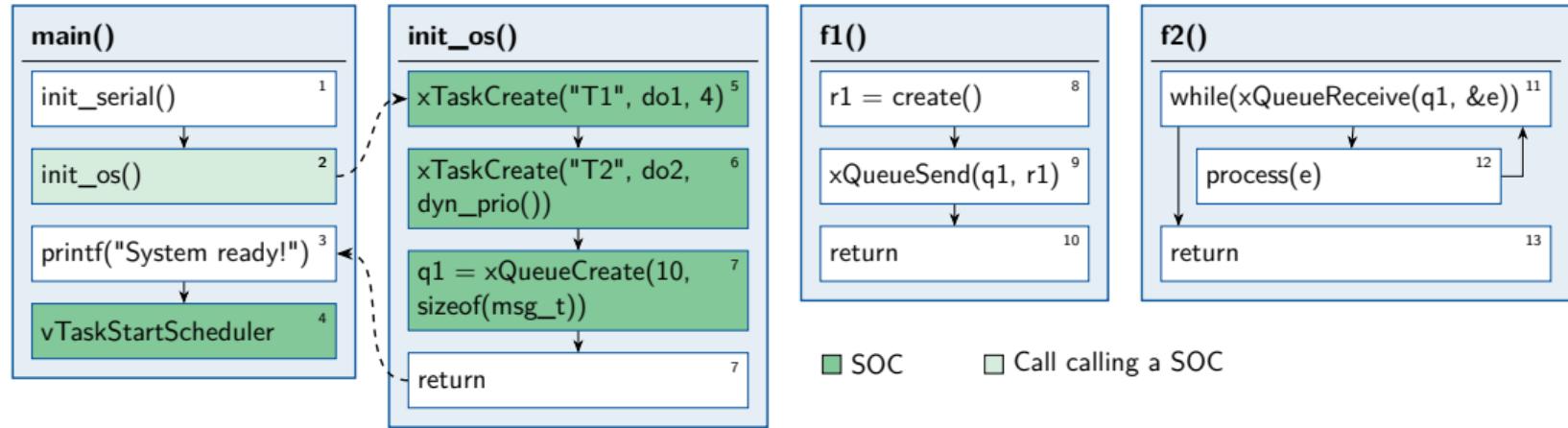
Complex pointer: Type based signature comparison



Difficulty: Function Pointer

Simple pointer: Andersen's pointer analysis

Complex pointer: Type based signature comparison



Entry Points:

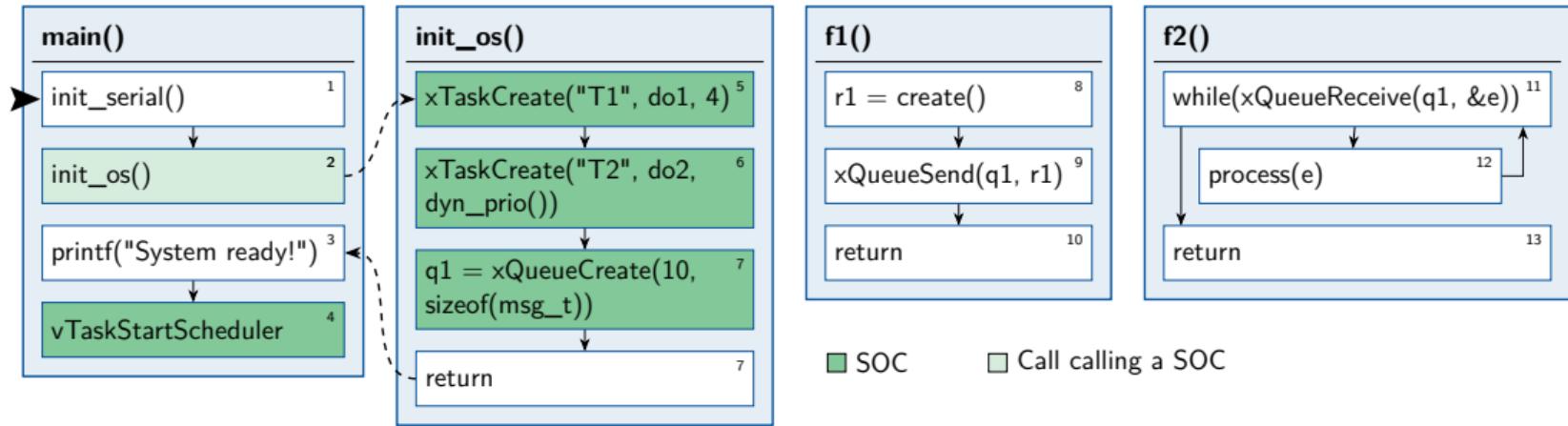
`main()`

Call Stack:

Parameter:

System Objects:

Created before scheduler
Created exactly once
RTOS allocates memory
All parameters known



Entry Points:

Call Stack:

_: main()

Parameter:

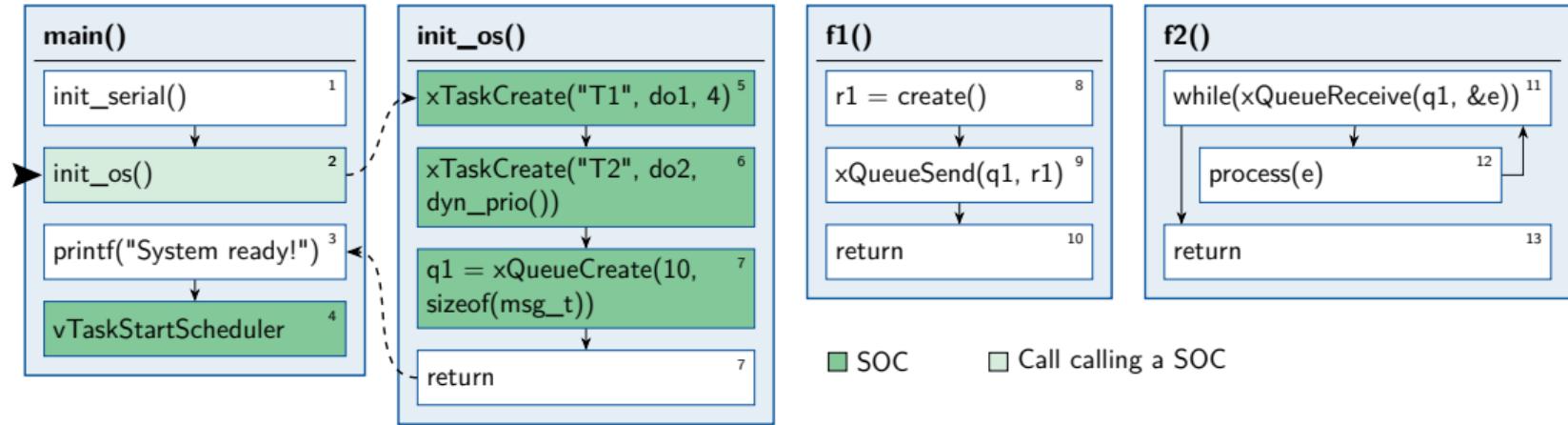
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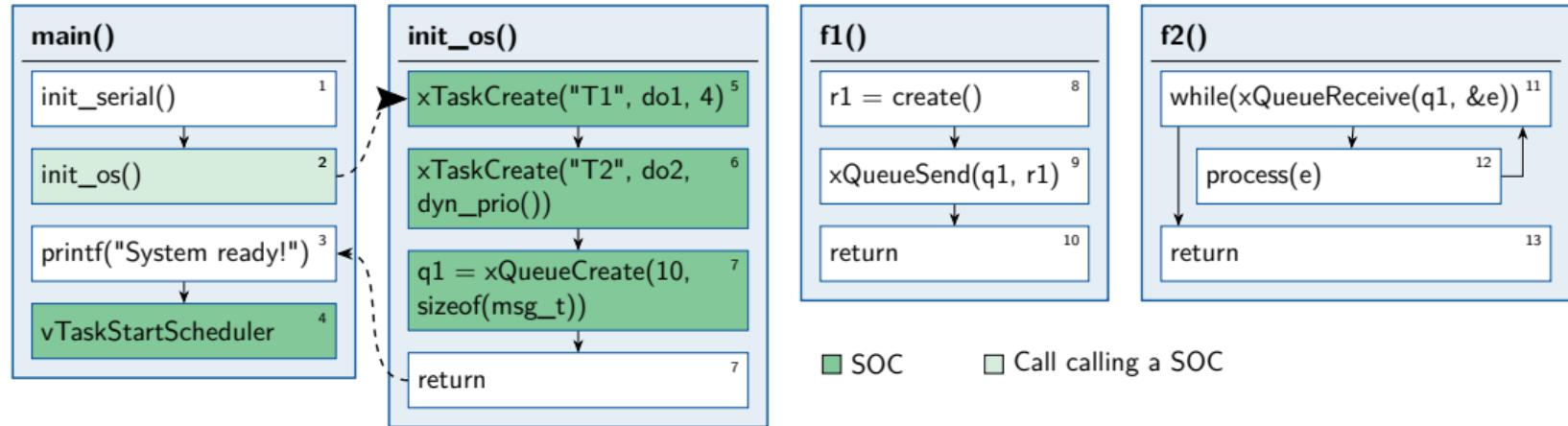
System Objects:

Created before scheduler

Created exactly once

RTOS allocates memory

All parameters known



Entry Points:

f1()

Call Stack:

_: main()
2: init_os()

Parameter: "T1", do1, 4

System Objects:

T1

Created before scheduler



Created exactly once

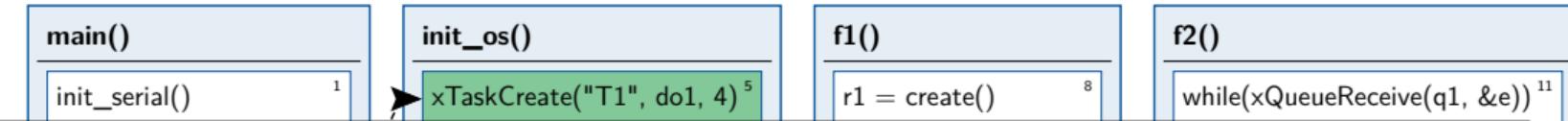


RTOS allocates memory



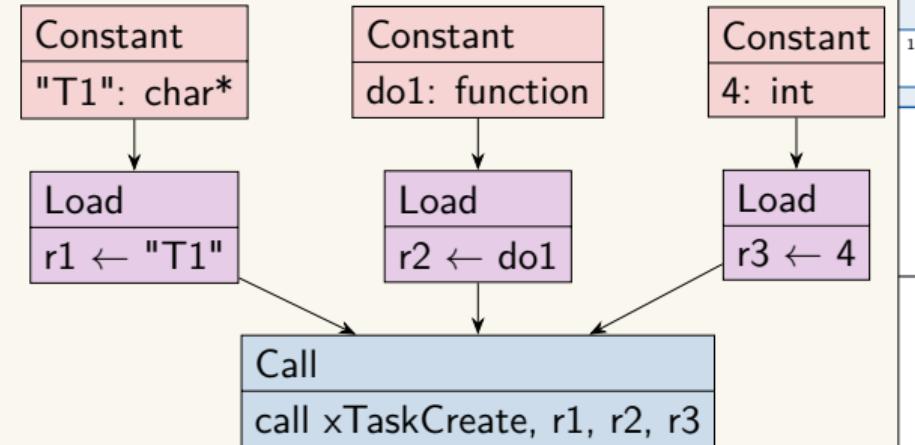
All parameters known



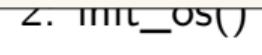


Value Analysis:

- ▶ Based on SVF[1] and LLVM-IR
- ▶ Traverse a Value Flow Graph
- ▶ Callpath aware



Call: `xTaskCreate("T1", do1, 4)`



Created exactly once



RTOS allocates memory

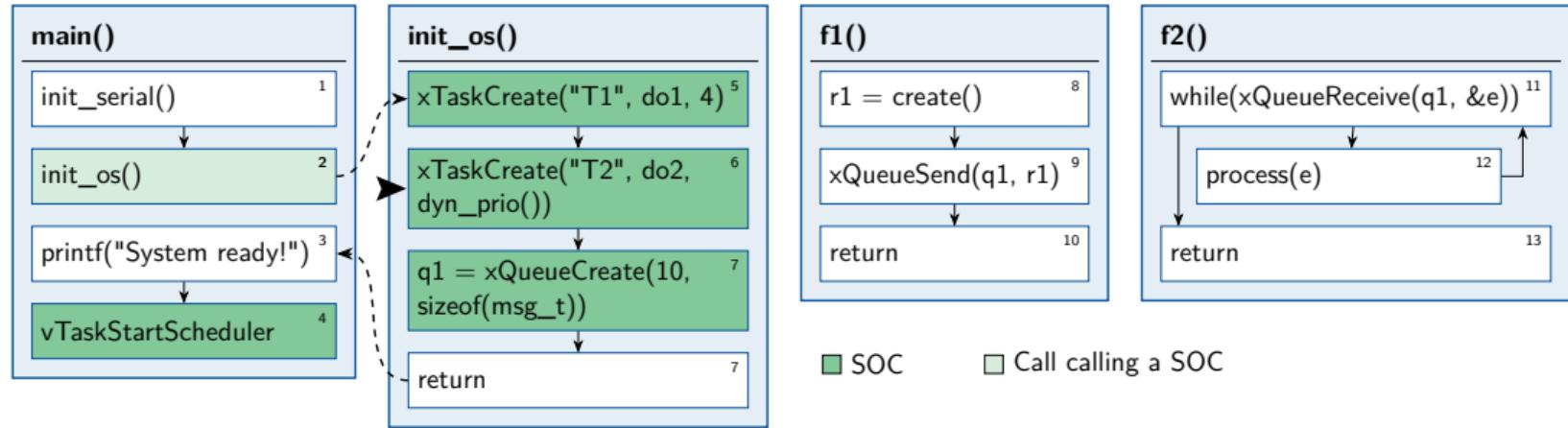


All parameters known



Parameter: `"T1", do1, 4`

Static Instance Analysis



Entry Points:

f1()
f2()

Call Stack:

_: main()
2: init_os()

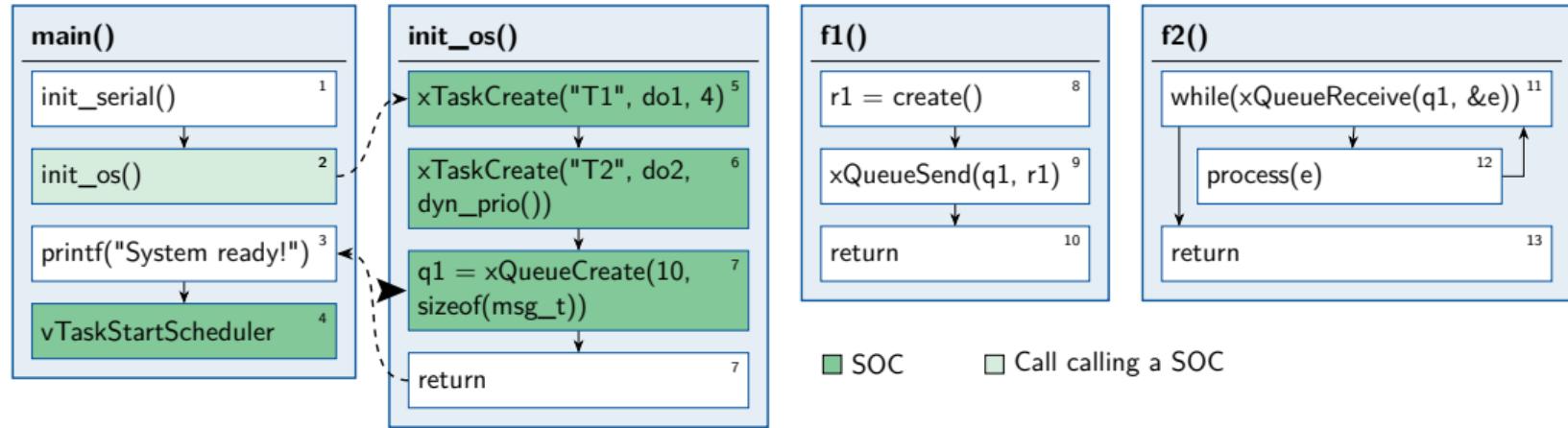
Parameter: "T2", do2, ?

System Objects:

Created before scheduler
Created exactly once
RTOS allocates memory
All parameters known

T1 T2

✓	✓
✓	✓
✓	✓
✓	X



Entry Points:

f1()
f2()

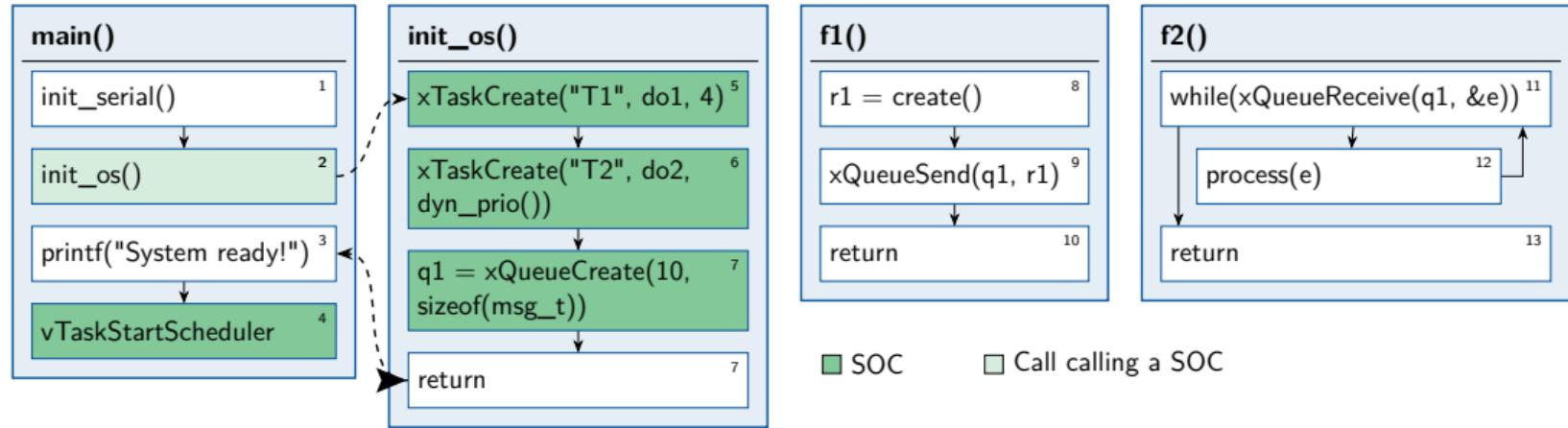
Parameter: 10, 8

Call Stack:

_: main()
2: init_os()

System Objects:

	T1	T2	Q1
Created before scheduler	✓	✓	✓
Created exactly once	✓	✓	✓
RTOS allocates memory	✓	✓	✓
All parameters known	✓	✗	✓



Entry Points:

`f1()`
`f2()`

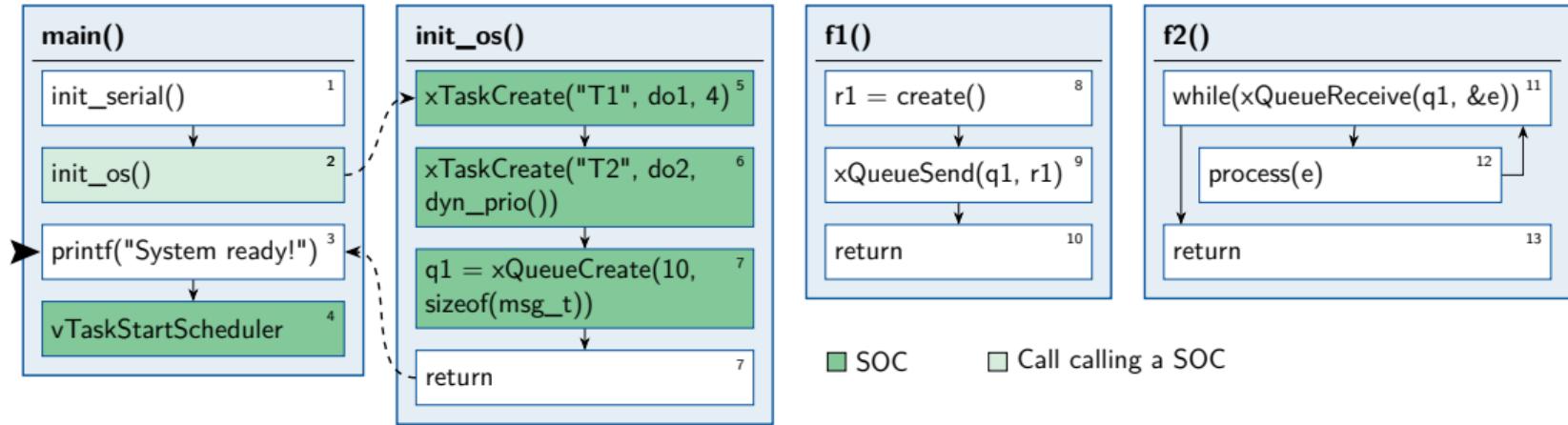
Call Stack:

_: `main()`
2: `init_os()`

Parameter:

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Entry Points:

f1()
f2()

Call Stack:

_: main()

Parameter:

System Objects:

Created before scheduler

T1 T2 Q1

✓ ✓ ✓

Created exactly once

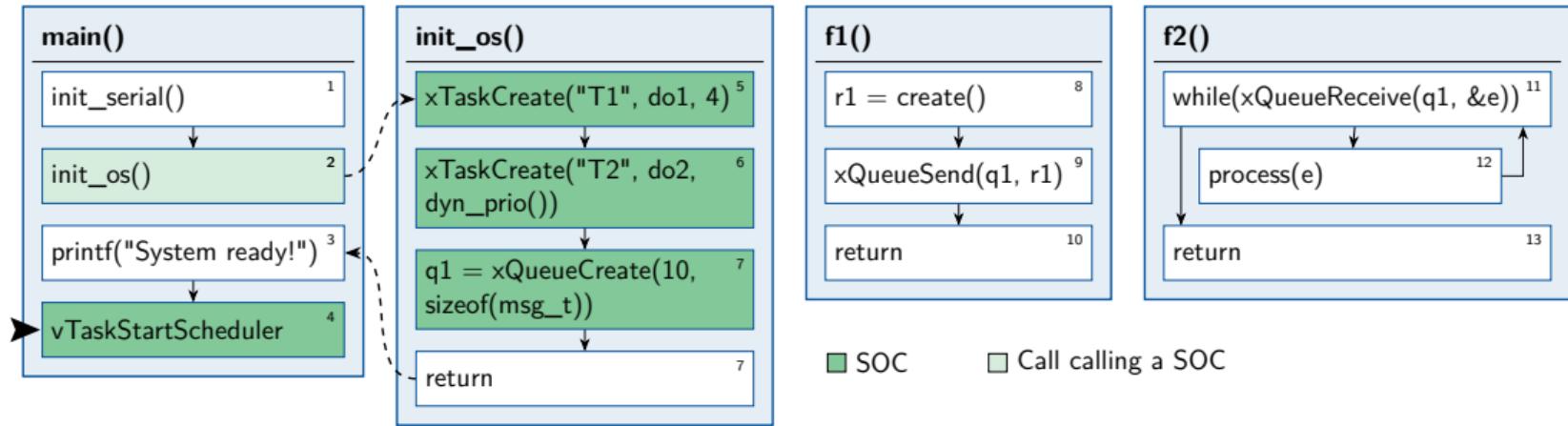
✓ ✓ ✓

RTOS allocates memory

✓ ✓ ✓

All parameters known

✓ ✘ ✓



Entry Points:

f1()
f2()

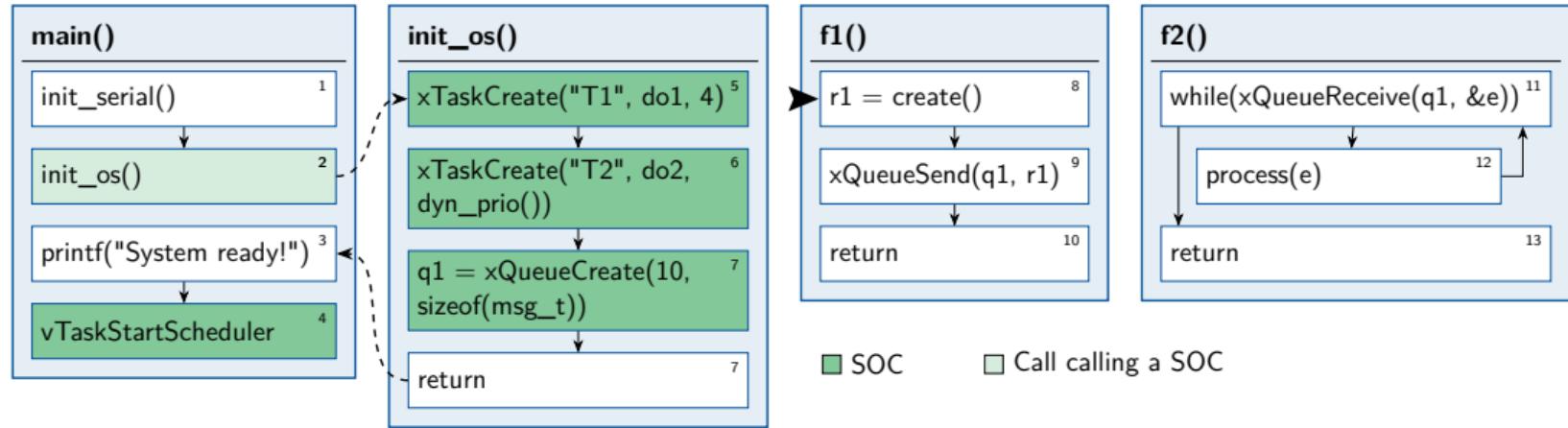
Call Stack:

_: main()

Parameter:

System Objects:

	T1	T2	Q1
Created before scheduler	✓	✓	✓
Created exactly once	✓	✓	✓
RTOS allocates memory	✓	✓	✓
All parameters known	✓	✗	✓



Entry Points:

f2()

Call Stack:

_: t1()

Parameter:

System Objects:

Created before scheduler

T1 T2 Q1

✓ ✓ ✓

Created exactly once

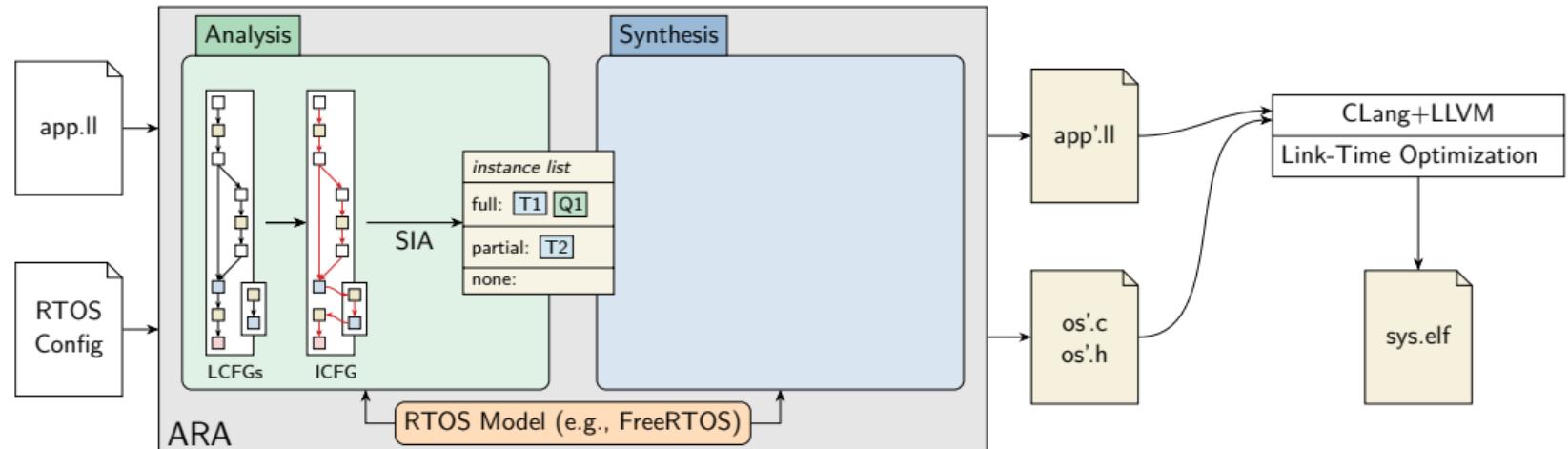
✓ ✓ ✓

RTOS allocates memory

✓ ✓ ✓

All parameters known

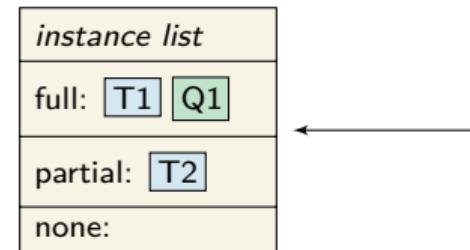
✓ X ✓



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Categorization of
Specialization Depth



<i>instance list</i>
full: T1 Q1
partial: T2
none:

```
+ InitStack_t<512> t1_stack(f2);

+ TCB_t t1_tcb = {
+   .StateListItem = {
+     .Previous = &ReadyLists[4].end,
+     .Next = &idle_task_tcb },
+   .Priority = 0,
+   .Stack = &t1_stack,
+   /* ... */
+ };

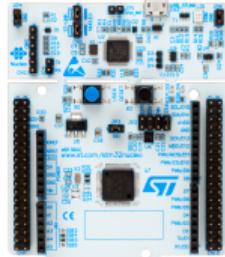
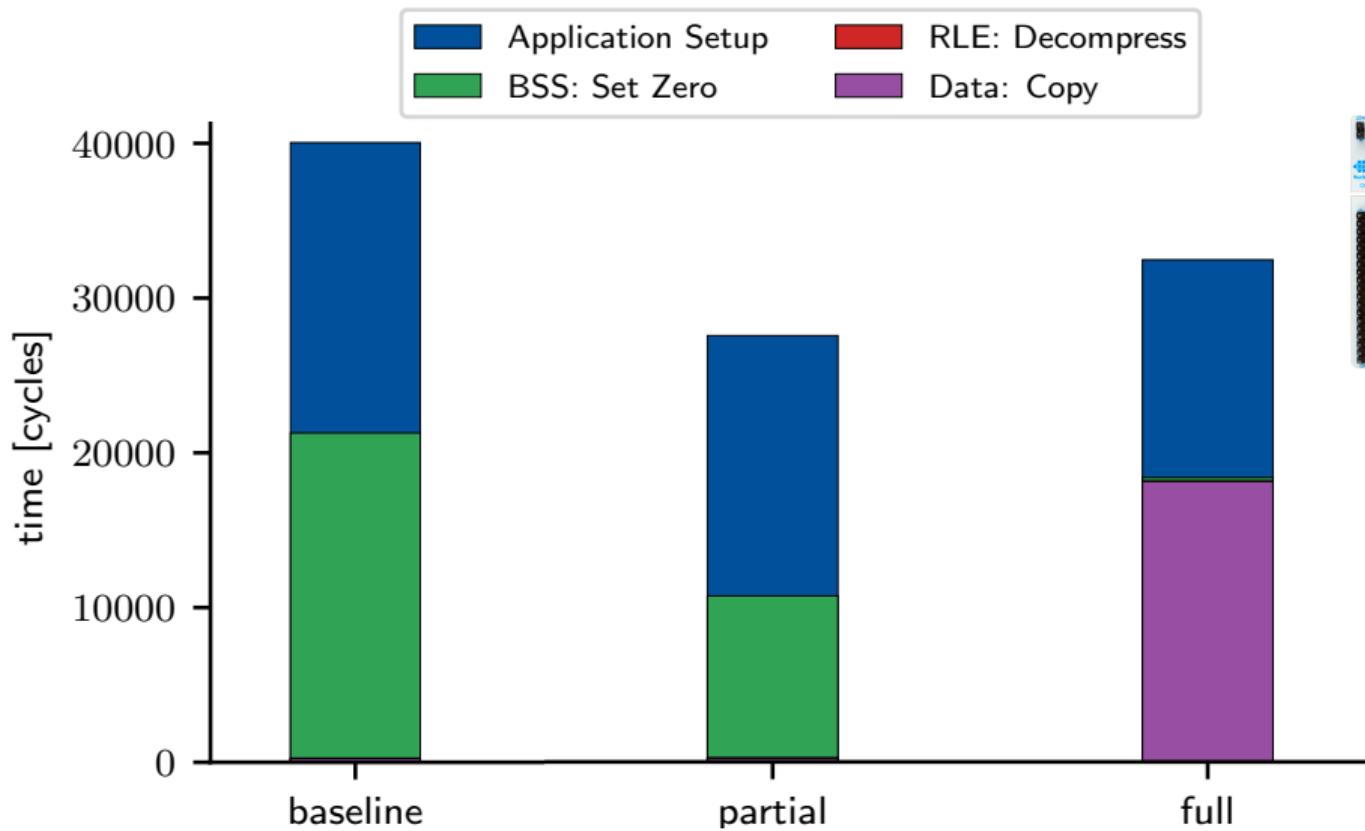
+ List_t ReadyLists[5] = { {
+   .ListEnd = {
+     .Next = &task2_tcb,
+     .Previous = &idle_task_tcb
+   },
+   .NumberOfItems = 2,
+   /* ... */
+ };
```

<i>instance list</i>
full: T1 Q1
partial: T2
none:

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+   /* ... */  
+ };  
  
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instance list
full: T1 Q1
partial: T2
none:

```
QueueHandle_t q1;  
  
void f1() { /* ... */ }  
void f2() { /* ... */ }  
  
void init_os() {  
-  xTaskCreate("T1", f1, 4);  
-  xTaskCreate("T2", f2, dyn_prio());  
+  xTaskActivate(t2_tcb, dyn_prio());  
-  q1 = xQueueCreate(10, sizeof(msg_t));  
}  
  
int main() {  
    init_serial();  
    init_os();  
    printf("System ready!");  
-  vTaskStartScheduler();  
+  vDispatch();  
}
```



■ Link-time optimization

- Constant propagation
 - Inlining

■ Sparse populated data

- OS objects (stack, queue, ...)
 - Libraries (SdFat, ...)

```
xTaskCreate("t1");
SdFatFile::SdFatFile() {
    mtx = xCreateMutex();
}
SdFatFile file();
```

ARA + LLVM

■ Link-time optimization

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■ Sparse populated data

- OS objects (stack, queue, ...)
 - Libraries (SdFat, ...)

■ Run-length encoding

- Less flash usage
 - Less copy time

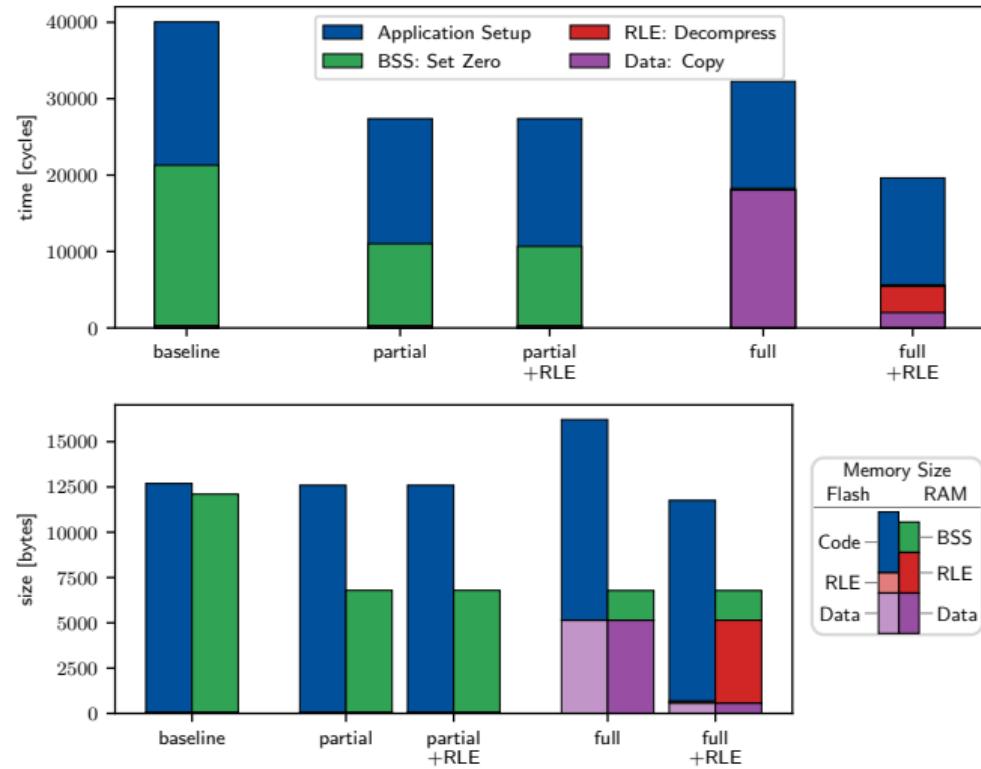
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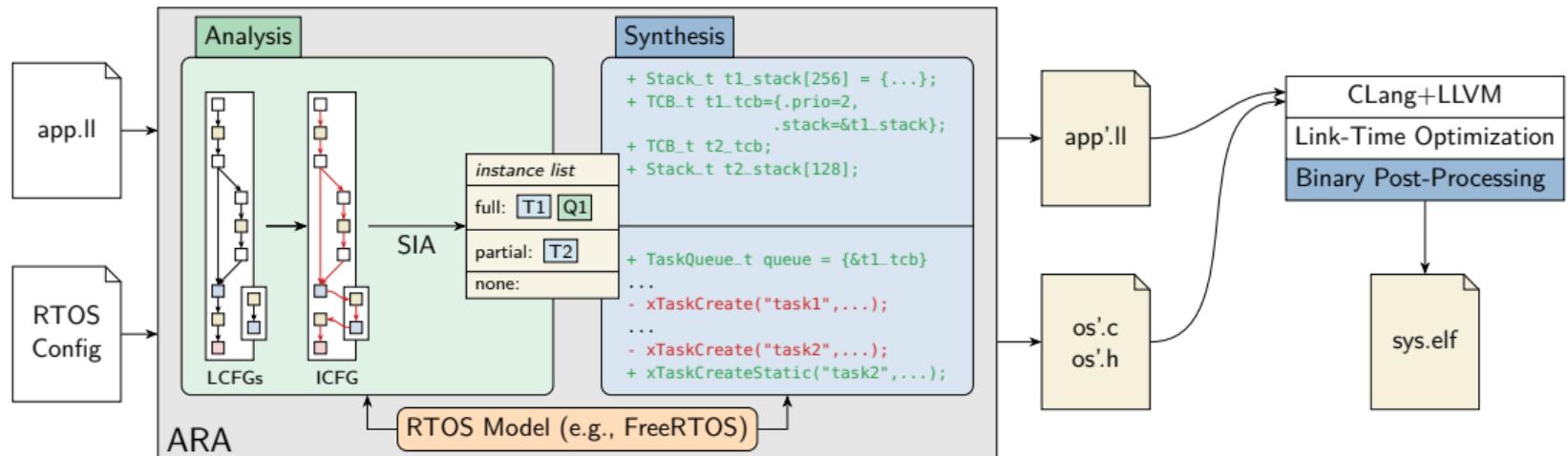
ARA + LLVM

ARA + LLVM + RLE

```
char stack1_comp = {(1<<31) + 2, arg, ret, 510, 0};  
char file_comp = {1, &mtx_anon_1, 342, 0};
```

- Link-time optimization
 - Constant propagation
 - Inlining
- Sparse populated data
 - OS objects (stack, queue, ...)
 - Libraries (SdFat, ...)
- Run-length encoding
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- Microbenchmarks

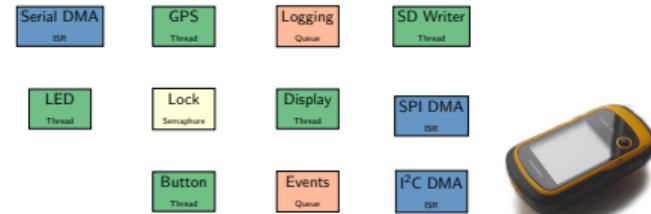
- Real-world applications

- Microbenchmarks
 - Queues
 - Tasks, pre scheduler
 - Tasks, post scheduler
- Real-world applications

```
1 int main() {  
2     xTaskCreate("T001", f1, ...);  
3     xTaskCreate("T002", f1, ...);  
4     xTaskCreate("T003", f1, ...);  
5     xTaskCreate("T004", f1, ...);  
6     xTaskCreate("T005", f1, ...);  
7     xTaskCreate("T006", f1, ...);  
8     xTaskCreate("T007", f1, ...);  
9     xTaskCreate("T008", f1, ...);  
10    xTaskCreate("T009", f1, ...);  
11    /* ... */  
12    xTaskCreate("T030", f1, ...);  
13    vTaskStartScheduler();  
14 }
```

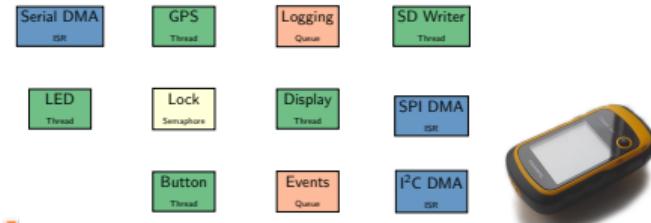
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 - GPSLogger

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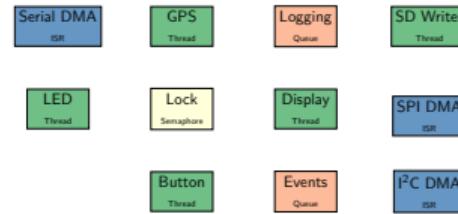
- Microbenchmarks
 - Queues
 - Tasks, pre scheduler
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- Real-world applications
 - GPSLogger
 - Librepilot

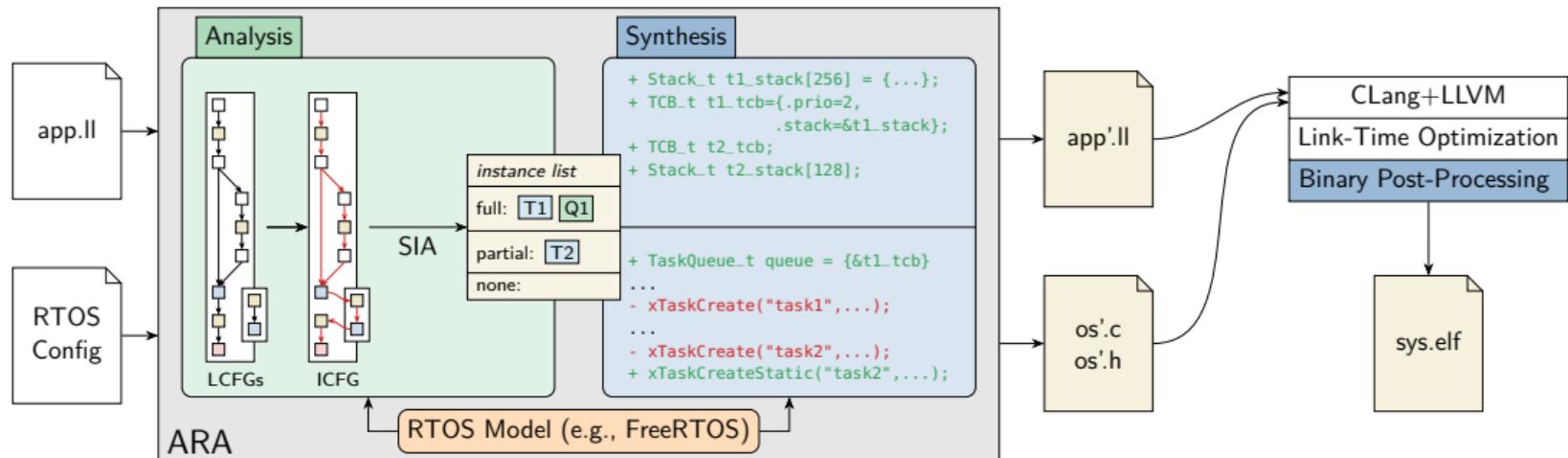
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- Microbenchmarks
 - Queues
 - Tasks, pre scheduler
 - Tasks, post scheduler
- Real-world applications
 - GPSLogger
 - Librepilot
- Result:
 - Up to 43% boot-time reduction
 - Moderate flash usage increase

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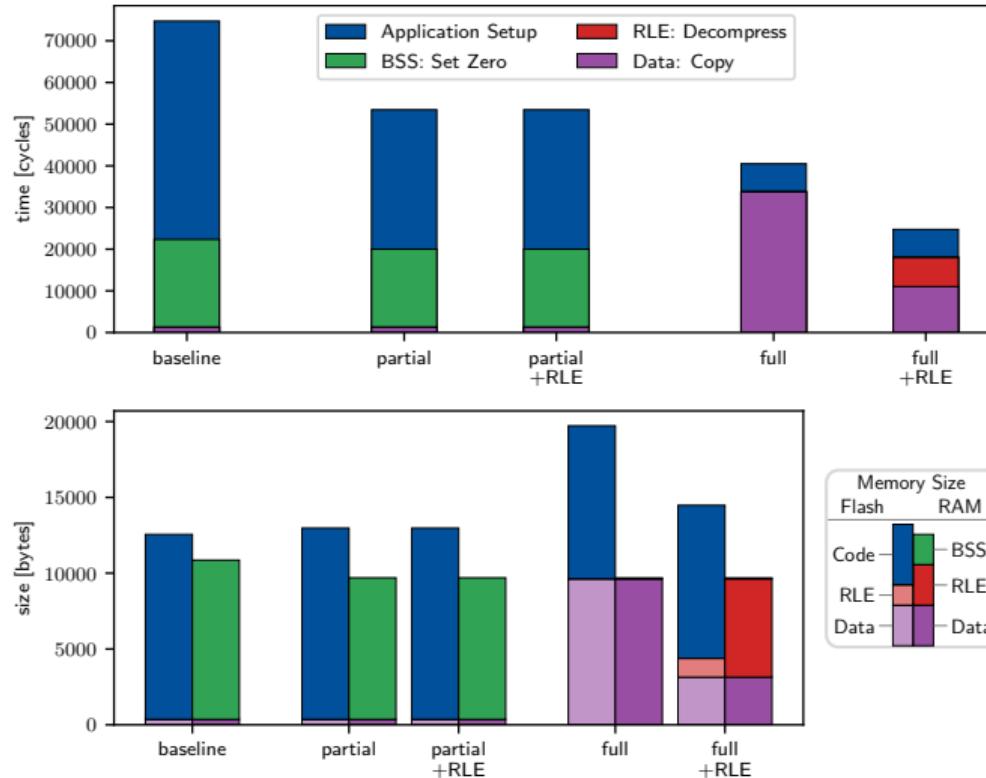




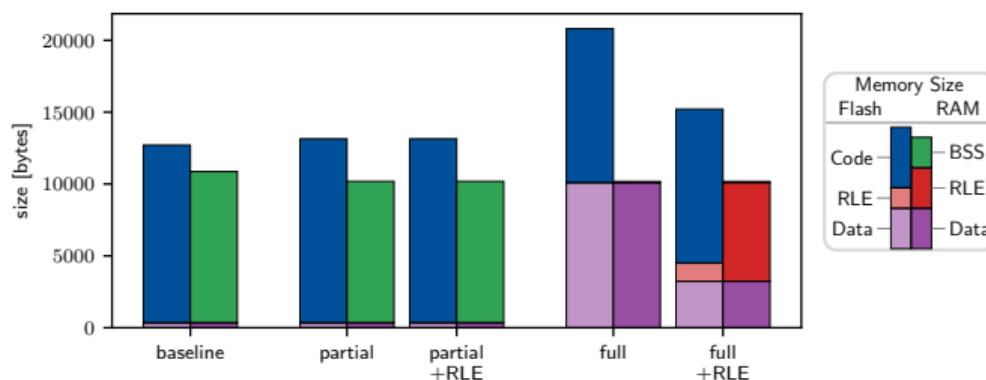
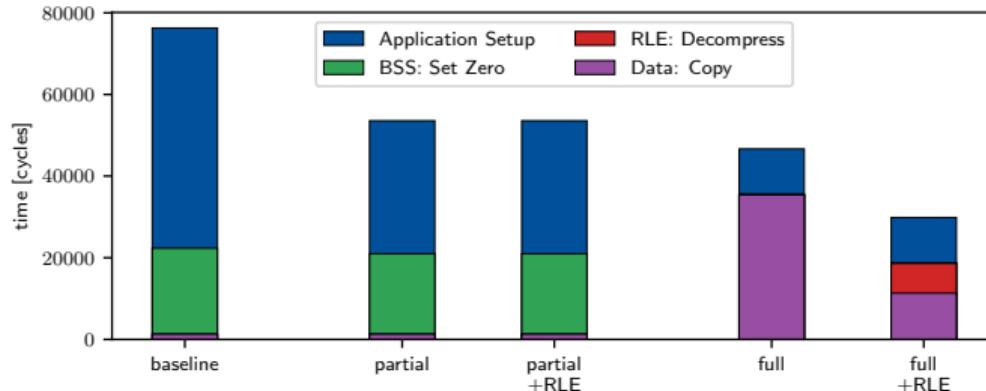
- Evaluated at microbenchmarks and two real-world applications
 - Up to 43% boot-time reduction
 - Moderate flash usage increase
- Open Source (<https://github.com/luhsra/ara>)
- Accepted at RTAS'21



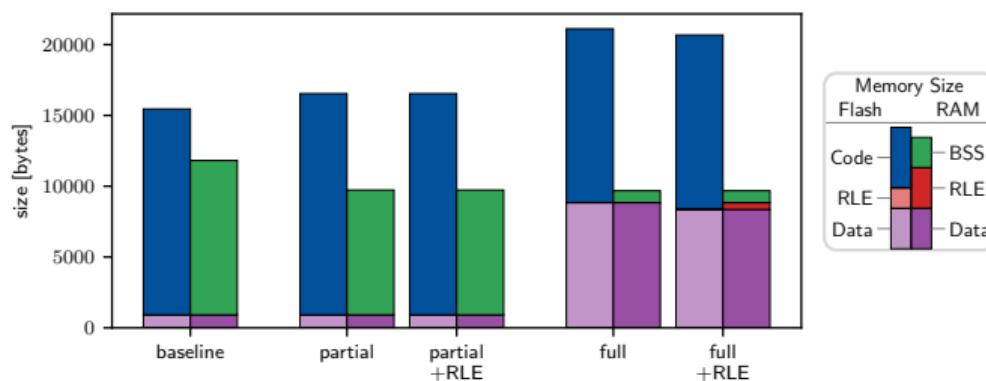
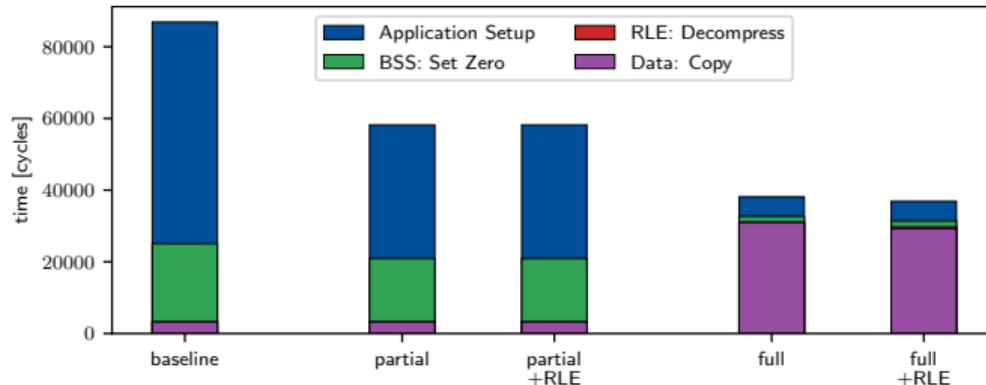
Evaluation – Tasks pre Scheduler

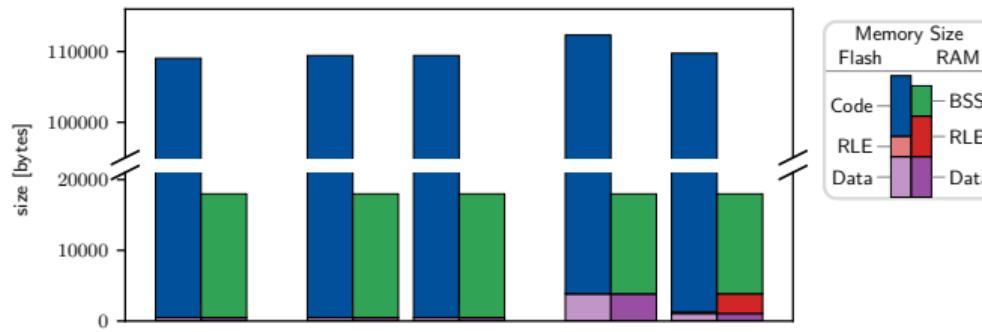
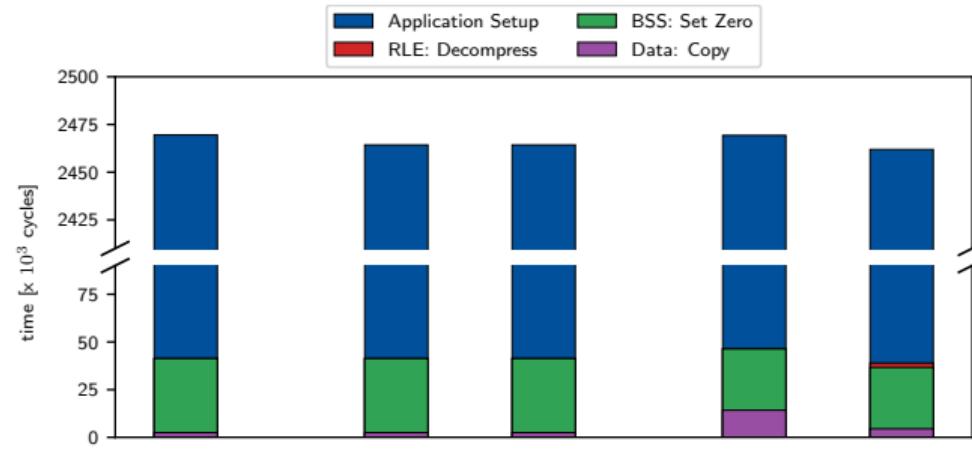


Evaluation – Tasks post Scheduler



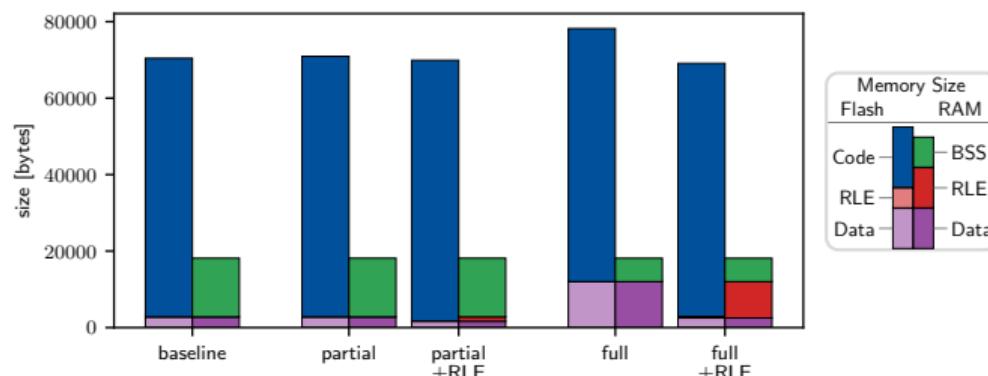
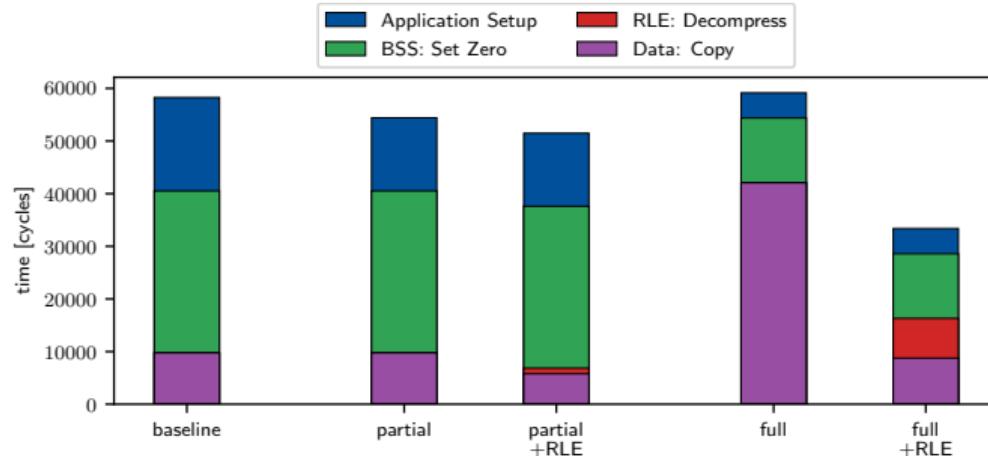
Evaluation – Queues





Memory Size	
Flash	RAM
Code	BSS
RLE	RLE
Data	Data

Evaluation – GPSLogger



Yulei Sui and Jingling Xue. "SVF: Interprocedural Static Value-Flow Analysis in LLVM". In: *Proceedings of the 25th International Conference on Compiler Construction*. CC 2016. Barcelona, Spain: Association for Computing Machinery, 2016, pp. 265–266. ISBN: 9781450342414. DOI: 10.1145/2892208.2892235.
URL: <https://doi.org/10.1145/2892208.2892235>.