

Synthesis of Optimized AUTOSAR Embedded Systems

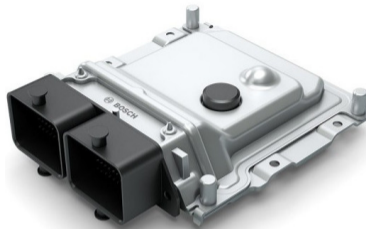
Fachgruppentreffen Bamberg 2023

Andreas Kässens

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29.09.2023

- Embedded real-time systems
- Large number of units
- Constrained hardware to save costs



AUTOSAR

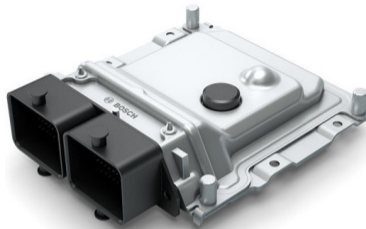


<https://www.bosch-mobility.com/de/loesungen/steuergeraete/motormanagementsysteme-2rad/>

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Goal

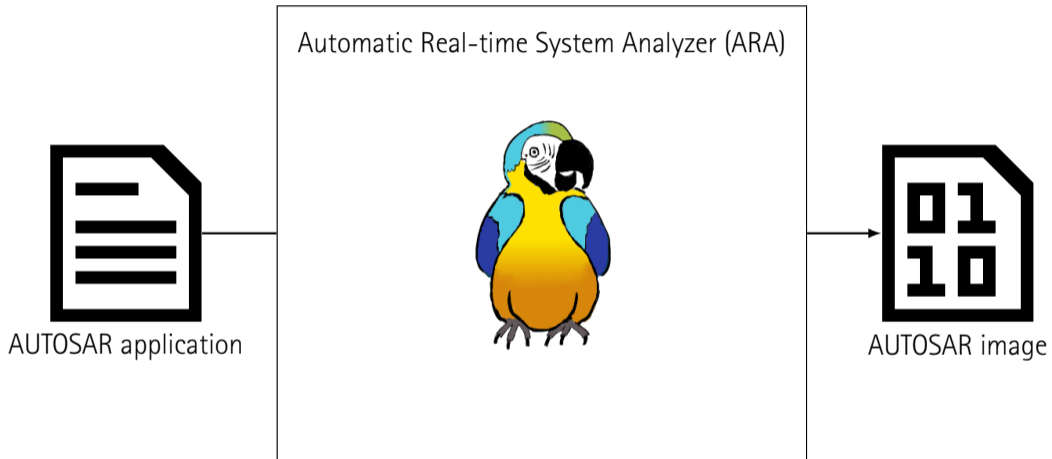
Optimization of non-functional properties

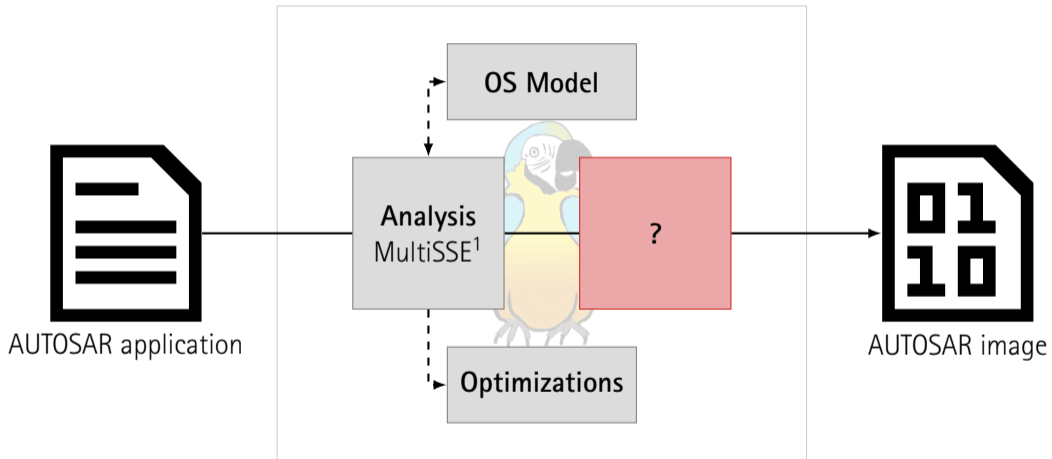


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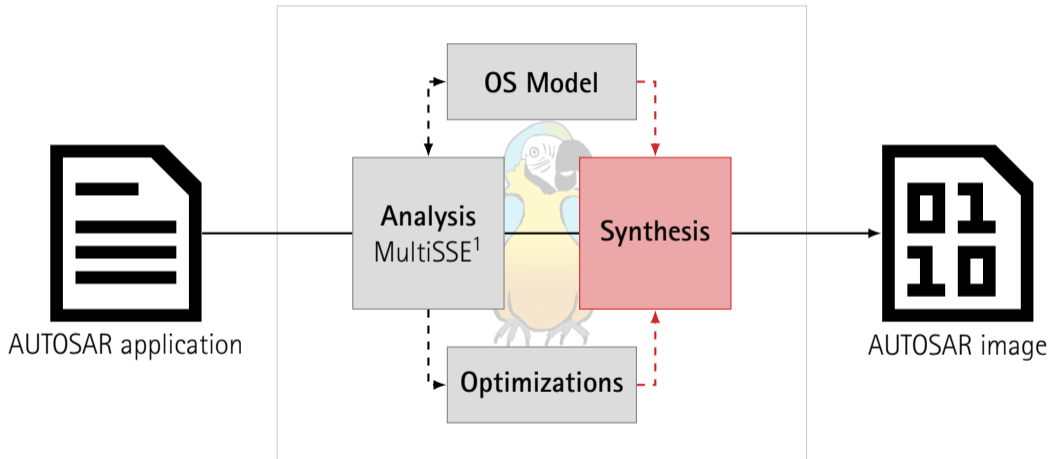


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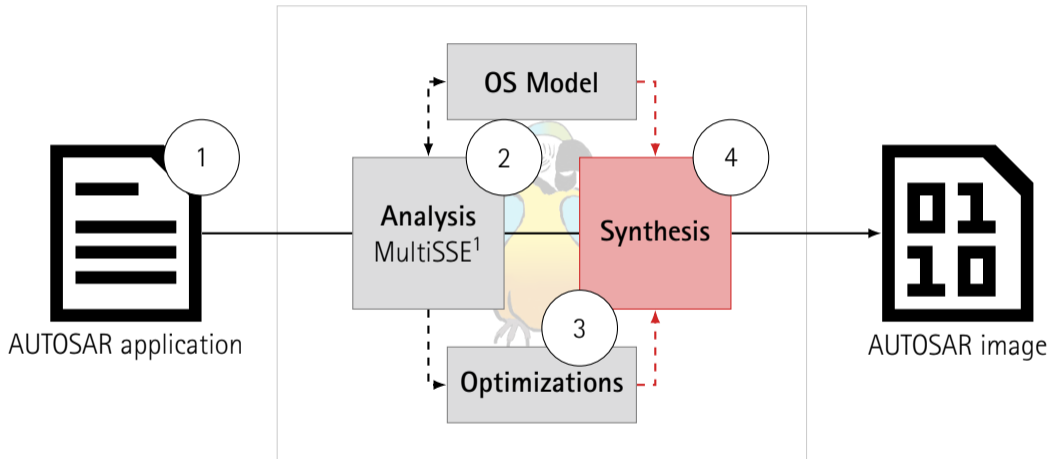




¹[EFL23] MultiSSE: Static Syscall Elision and Specialization for Event-Triggered Multi-Core RTOS



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■ Static configuration of RTOS objects



```
cpus:  
- id: 0  
  tasks:  
  - T01:  
    autostart: true  
    priority: 1  
    spinlocks: [S1]  
- id: 1  
  tasks:  
  - T12:  
    autostart: false  
    priority: 2  
    spinlocks: [S1]  
  - T13:  
    autostart: false  
    priority: 3  
    spinlocks: []
```

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1 TASK(T01) {  
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 - Pipeline of analysis passes
 - LLVM Intermediate Representation (IR)



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 - Static application analysis with OS model
 - Result: Multicore State Transition Graph (MSTG)
 - Enables cross-core system call optimization



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 - Pipeline of analysis passes
 - LLVM Intermediate Representation (IR)
- MultiSSE: enumerate all possible system states
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 - Result: Multicore State Transition Graph (MSTG)
 - Enables cross-core system call optimization
- Synthesis only for FreeRTOS¹

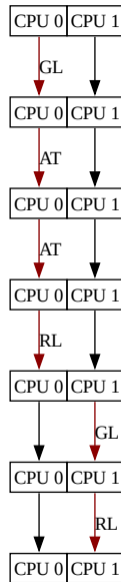
¹[Fie+21] ARA: Static Initialization of Dynamically-Created System Objects



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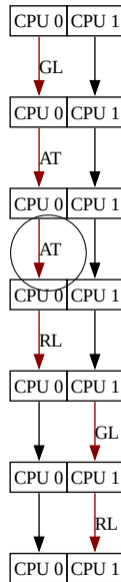


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AT = ActivateTask


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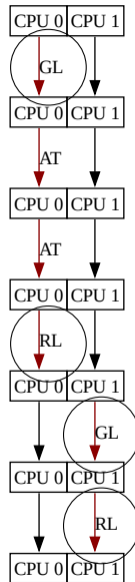


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Goal

Optimization of non-functional properties

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Remove costly but unnecessary operations to improve timing



Generated by DALL-E

Goal

Optimization of non-functional properties

Remove costly but unnecessary operations to improve timing

- Lock Elision
 - Lock is never spinning between Get- and ReleaseSpinlock
 - **Remove locking operations!** (or system calls)



Generated by DALL-E

Goal

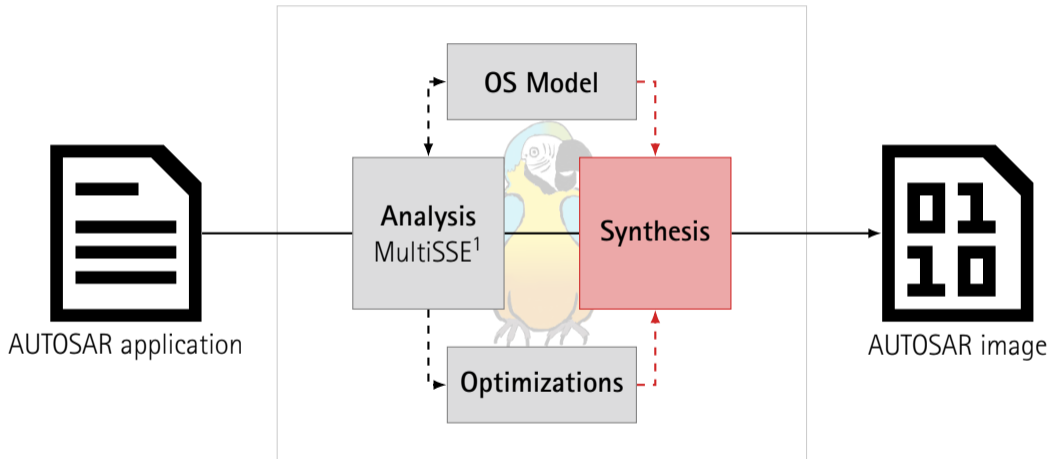
Optimization of non-functional properties

Remove costly but unnecessary operations to improve timing

- Lock Elision
 - Lock is never spinning between Get- and ReleaseSpinlock
 - Remove locking operations! (or system calls)
- IPI Avoidance
 - Cross-core system calls like `ActivateTask`
 - IPI does not lead to rescheduling
 - Don't trigger IPI!



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¹[EFL23] MultiSSE: Static Syscall Elision and Specialization for Event-Triggered Multi-Core RTOS

Multicore **A**UTOSAR **C**ompatible **A**pplication-specific **W**hole-system-optimizer (MACAW¹)

¹Macaws are a group of New World parrots that are long-tailed and often colorful. (Wikipedia)

Multicore **A**UTOSAR **C**ompatible **A**pplication-specific **W**hole-system-optimizer (MACAW¹)

- AUTOSAR specification
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 - OS objects, RTOS functionality
 - Interrupts
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- System call optimization

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- System call optimization
- Extend *d*OSEK²
 - OSEK: single-core only
 - System call specialization for dependability
- POSIX platform
 - Cores → Threads
 - Interrupts → Signals

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²[Hof+15] *d*OSEK: The Design and Implementation of a Dependability-Oriented Static Embedded Kernel

- Generic AUTOSAR functionality
 - Tasks, Events, Spinlocks, ...
 - Per-core scheduling and timers, IPLs, multicore handling

System Libraries

IR Modification

Generator

- Generic AUTOSAR functionality
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- Modification of application code
 - Specialized call sites: `ActivateTask` → `ActivateTask_BB18`
 - Automatic insertion: initialization, kickoff, error handling

System Libraries

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- System generation
 - Connecting the application to the AUTOSAR libraries
 - Application-specific instantiation and specialization

System Libraries

IR Modification

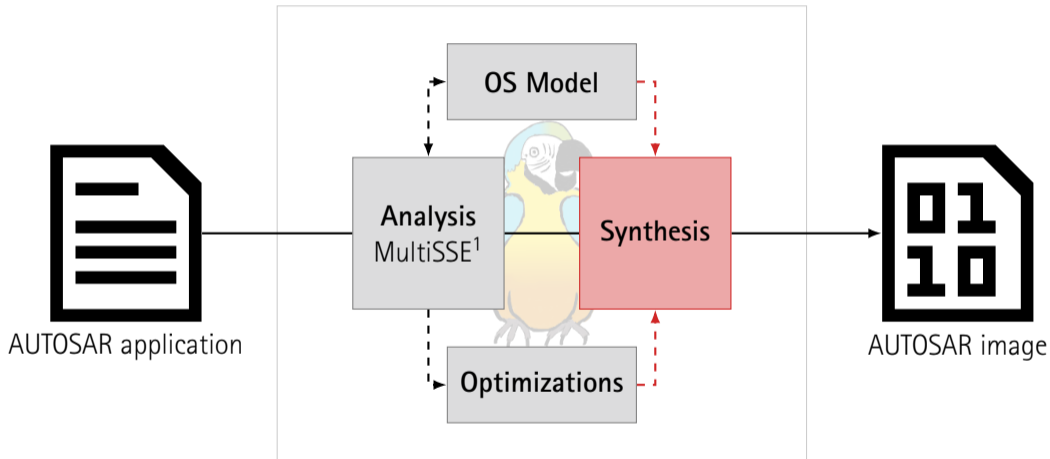
Generator

- Analysis results:
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 - Locking needed?

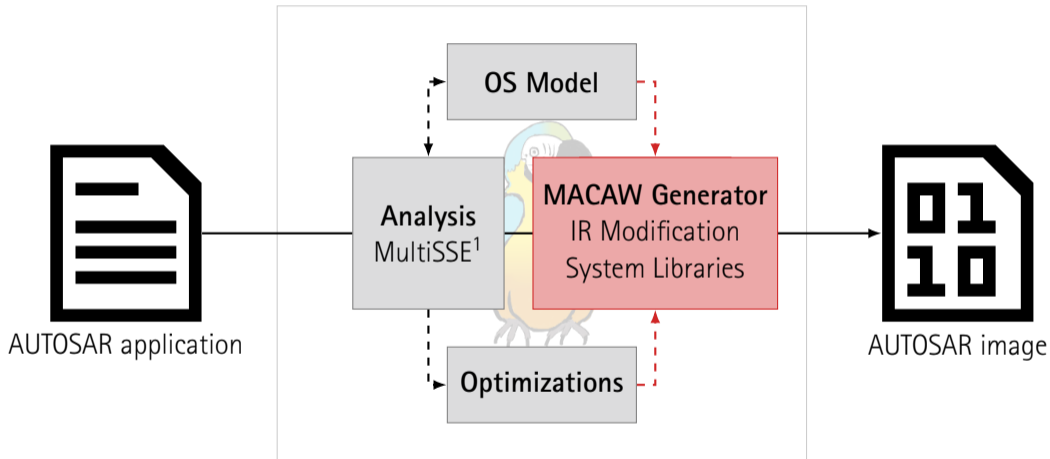
```
1 StatusType AUTOSAR_ActivateTask_BB18(TaskType arg0)
2 {
3     StatusType result = E_OK;
4     Machine::disable_interrupts();
5
6     scheduler_[1].SetReady_impl(OS_T10_task);
7     Machine::trigger_interrupt(SIGUSR1, 1, true);
8
9     Machine::enable_interrupts();
10    return result;
11 }
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- Analysis results:
 - IPI needed?
 - Locking needed?
- Elision of unnecessary operations
 - Reduced kernel path overhead
 - Improved timing

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- Synthesis supports all AUTOSAR model features
- Test applications



Real-world applications	✗
dOSEK tests	✓
Trampoline tests ¹	(✓)
MultiSSE tests	✓
MACAW tests	✓

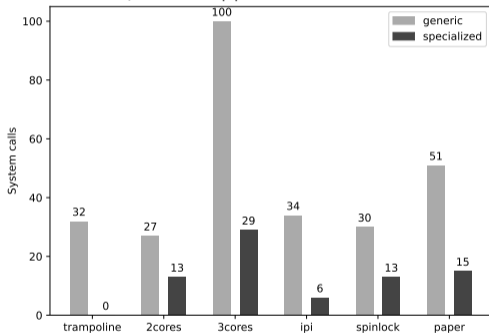
Ok:	105
Expected Fail:	0
Fail:	0
Unexpected Pass:	0
Skipped:	0
Timeout:	0

Not compiled:	9

¹Missing AUTOSAR features: ScheduleTable, Timing Protection, TerminateApplication

■ How many system calls can be specialized?

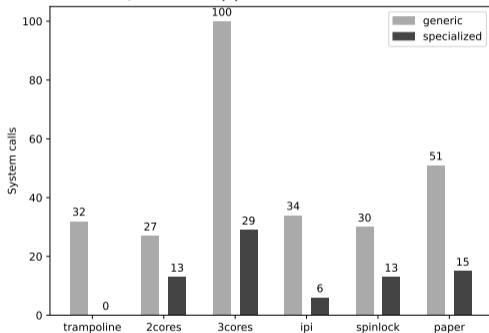
■ Analyze test applications



■ 28 % of relevant system calls

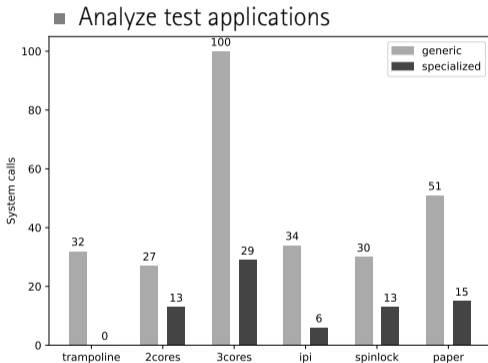
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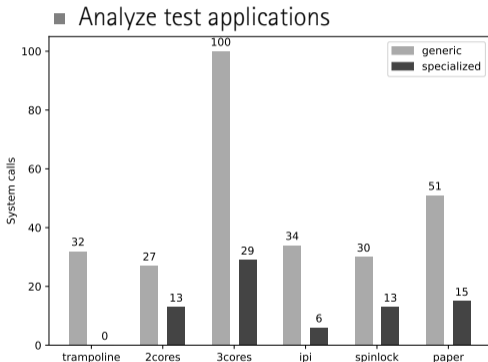


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- Microbenchmarks:
 - 2.5 μ s for synchronized IPI
 - 0.01 μ s for Spinlock

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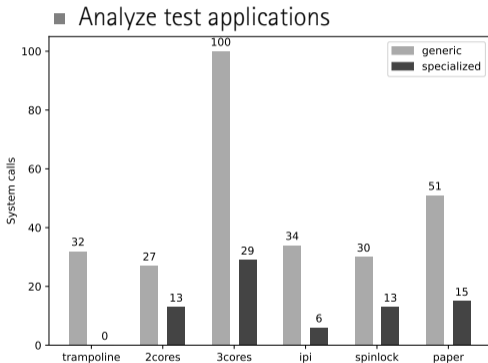
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ipi2_f	22 μ s	19 μ s
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spinlock_a	29 μ s	29 μ s

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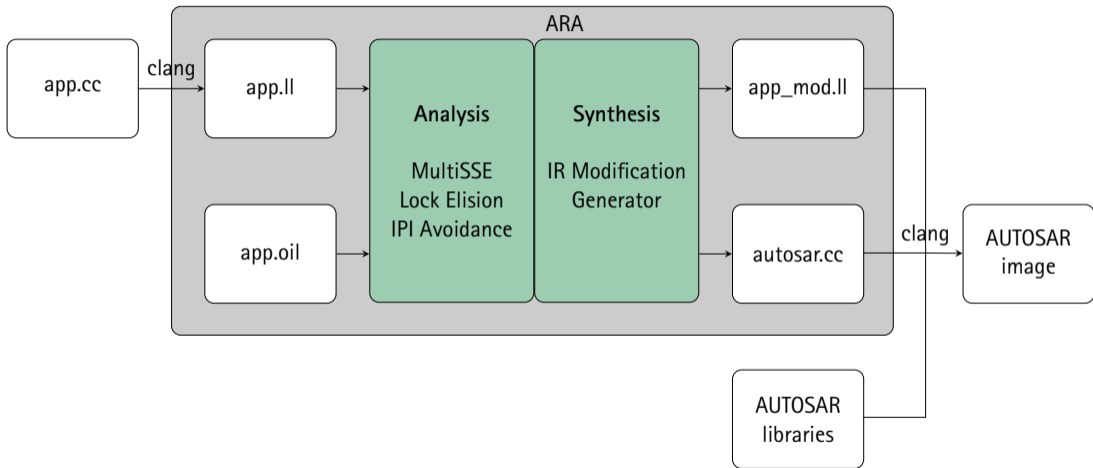
Thank You

```
_Atomic(int) cores = 1;
_Atomic(int) syncing_cores[arch::SYNC_MAX] = {0};

void sync_all_hardware_threads(cpu_sync_point_t sync_point) {
    syncing_cores[sync_point]++;
    // wait until all cores have synced
    while (syncing_cores[sync_point] < cores);
}
```

```
Function* kickoff = Function::Create(void_type_void,  
    Function::ExternalLinkage,"AUTOSAR_kickoff", &module);  
  
for (auto& function : module) {  
    // all TASK(foo) functions start with this string, see os/os.h  
    if (function.getName().startswith("AUTOSAR_TASK_FUNC")) {  
        IRBuilder<> builder(context);  
        BasicBlock& bb = *function.begin();  
        Instruction* inst = &*bb.begin();  
        builder.SetInsertPoint(inst);  
        std::vector<Value*> ArgsV;  
        builder.CreateCall(kickoff, ArgsV);  
        /* ... */  
    }  
}
```

```
void IRQ::trigger_interrupt(int irq, int cpuid, bool sync) {  
    if (!cpu_online(cpuid)) {  
        debug_core << "cpuid " << cpuid << " offline" << endl;  
        return;  
    }  
  
    if (!sync) {  
        pthread_kill(get_thread_id(cpuid), irq);  
        return;  
    }  
  
    /* synchronized interrupt: wait until target core does reschedule */  
    ipi_cleared[cpuid] = false;  
    pthread_kill(get_thread_id(cpuid), irq);  
    while (ipi_cleared[cpuid] == false);  
}
```



- Insertions in the application's IR code
 - Kickoff function on `Task` function entry
 - Error handling on return from `Task`
 - Architecture-specific initialization in `main()`

System Libraries

IR Modification

Generator

- Insertions in the application's IR code
 - Kickoff function on `Task` function entry
 - Error handling on return from `Task`
 - Architecture-specific initialization in `main()`
- System call renaming
 - IR: Functions are split into Basic Blocks
 - Rename function name at each system call site
 - `ActivateTask` → `ActivateTask_BB18`
 - Specialization per call site possible

System Libraries

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- System generator (based on *d*OSEK)
 - Access ARA data structures and ValueAnalyzer results
 - Modify templates
 - Add new system calls

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 - Add new system calls
- Generator components
 - Generic OS instances
 - Architecture-specific code
 - System calls

System Libraries

IR Modification

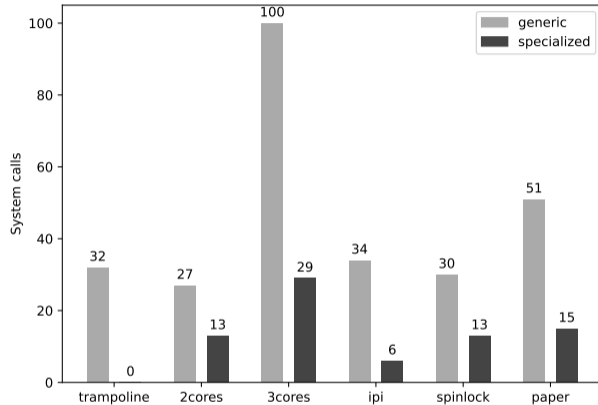
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- System generator (based on *dOSEK*)
 - Access ARA data structures and ValueAnalyzer results
 - Modify templates
 - Add new system calls
- Generator components
 - Generic OS instances
 - Architecture-specific code
 - System calls
- Result: `autosar.cc`

System Libraries

IR Modification

Generator



- 76/274 → 28 %
- Mostly Lock Elision

■ POSIX Microbenchmarks

IPI	1.7 ms
IPI (sync)	2.5 ms
Spinlock	0.01 ms

■ Test applications

Application	Generic	Specialized	Description
ipi2_g	22 μ s	7 μ s	1 / 1 IPIs avoided
ipi2_f	22 μ s	19 μ s	1 / 2 IPIs avoided
ipi2_d	26 μ s	23 μ s	1 / 3 IPIs avoided
spinlock_a	29 μ s	29 μ s	0 / 1 IPIs avoided, 4 / 8 (un)locks elided