

**New Research Group:
System Software
at Friedrich Schiller University Jena**

Clemens Grelck



**FRIEDRICH-SCHILLER-
UNIVERSITÄT
JENA**

**Herbst-Treffen der GI-Fachgruppe Betriebssysteme
Bamberg, September 2023**

Some words about me ...



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Professor for system software:

- ▶ Started April 2023
- ▶ Research group in the making...
- ▶ We're hiring ...

System software:

- ▶ Bridging the gap between hardware and application software
- ▶ Operating systems
- ▶ Programming languages
- ▶ Compilers
- ▶ Runtime systems
- ▶ ...

Friedrich-Schiller-University Jena:

- ▶ Founded in 1564
- ▶ 17,000 students
- ▶ 16 professors in computer science
- ▶ Only full university in Thüringen

Some words about me ...

But how did I get here?



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Christian-Albrechts-Universität zu Kiel



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1990–2001

- ▶ Student of informatics and economics
- ▶ Diploma in informatics
- ▶ Wissenschaftlicher Angestellter
- ▶ Dr.rer.nat



Universität zu Lübeck



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2001–2008

- ▶ Wissenschaftlicher Angestellter
- ▶ Wissenschaftlicher Assistent (C1)



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University of Hertfordshire, Hatfield, United Kingdom



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2006–2009

- ▶ Research Fellow
- ▶ Principal Lecturer



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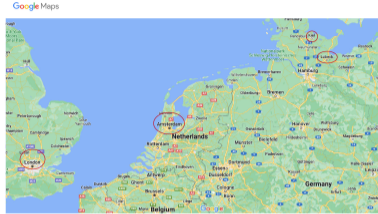
Universiteit van Amsterdam, Nederlande



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2008–2023

- ▶ Assistant Professor
- ▶ Associate Professor



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And finally ...



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So much about me ...

But what about my research ?

My research agenda

System software to the rescue:

- ▶ Domain-specific programming languages and abstractions
- ▶ Optimising compilers / static analysis
- ▶ Adaptive runtime systems / dynamic analysis and control

... targeting (heterogeneous) parallel architectures

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... targeting (heterogeneous) parallel architectures

With non-functional requirements:

- ▶ time: speed / deadline
- ▶ energy: saving / budget
- ▶ robustness / fault-tolerance
- ▶ ...

Programming Language SAC: Single Assignment C

Key design choices:

- ▶ Purely functional semantics with C-like syntax
- ▶ Data-parallel array programming
- ▶ Targeting compute-intensive applications
- ▶ Immutable, truly multi-dimensional arrays
- ▶ **Radically different: completely target- and resource-agnostic**

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- ▶ parallelisation code
- ▶ synchronisation/communication code
- ▶ target hardware-specific arrangements
- ▶ ... and all the other clutter

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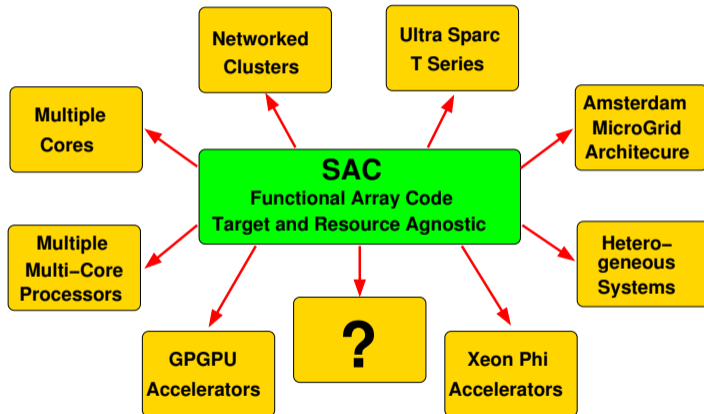
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Benefits:

- ▶ No race conditions
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- ▶ No deadlocks
- ▶ No you name it
- ▶ **Higher confidence in functional correctness**
- ▶ **Parallel execution for free**

Single Assignment C means ...

One source — many compilation targets:

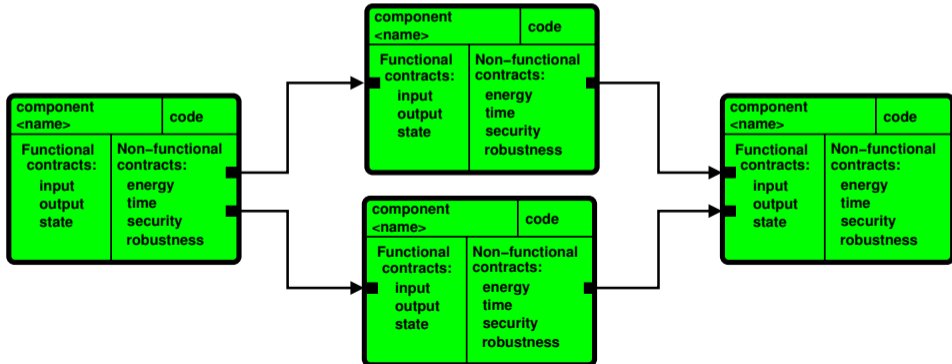


... and achieve reasonable performance for free !!

Coordination Language TeamPlay

Based on the concept of Exogeneous Coordination:

- ▶ Component identification
- ▶ Component interaction following **data-flow principle**
- ▶ Reasoning about **non-functional properties: time, energy, robustness, ...**
- ▶ Targeting cyber-physical systems



Heterogeneous Multi-core Coordination

Central question:

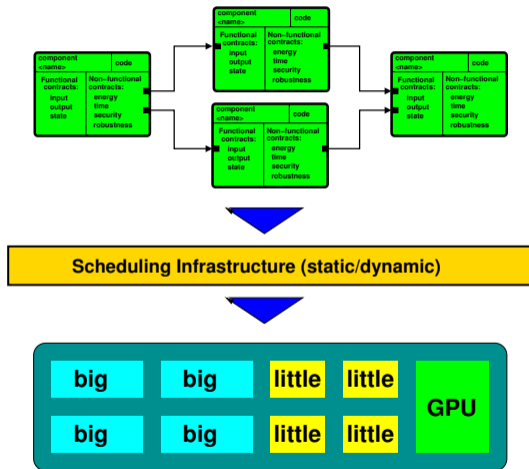
- ▶ What to run where and when?
... and why?

Constraints and objectives:

- ▶ Energy: want to minimize
- ▶ Time: must meet the deadline
- ▶ Maybe more ...

DVFS:

- ▶ Choose voltage/frequency pairs
- ▶ Per voltage/frequency island
- ▶ Change voltage/frequency over time



Energy Consumption Matters

Mobile devices:



- ▶ Battery-powered devices
- ▶ Less energy, more time to operate

Energy Consumption Matters

Mobile devices:



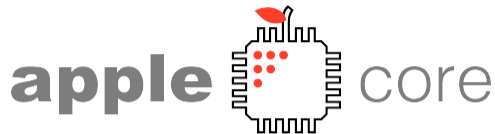
- ▶ Battery-powered devices
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Sustainable computing:



- ▶ Computing takes an ever increasing share of the world's energy resources.

Acknowledgments



TEAMPLAY



Time, Energy and security Analysis for
Multi/Many-core heterogeneous PLAtforms

